



HIT 2 482-06

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application:

T. WATANABE et al

Serial No. 09/198,658

Filed: November 24, 1998

Group Art Unit:

2783

Examiner:

E. Coleman

For:

NEURAL NETWORK PROCESSING SYSTEM

USING SEMICONDUCTOR MEMORIES

## INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure, Applicants inform the Examiner of the documents cited during prosecution of the parent application, Serial No. 09/198,658.

The applicants request the Examiner to initial and return a copy of the attached PTO-1449 form to indicate that the references have been considered.

Respectfully submitted,

Shrinath Malur Registration No. 34,663 Attorney for Applicants

MATTINGLY, STANGER & MALUR 104 East Hume Avenue Alexandria, Virginia 22301 (703) 684-1120 Date: December 20, 2000